



**DECLARATION FOR PATENT APPLICATION**  
**English Language Declaration**

As a below-named inventor, I hereby declare that:

My residence, mailing address, and citizenship are as stated below next to my name.

I believe that I am the original, first, and sole inventor (if only one name is listed below) or an original, first, and joint inventor (if plural names are listed below) of the subject matter that is claimed and for which a patent is sought on the invention entitled:

**METHOD OF ANALYZING A WAFER FOR METAL IMPURITIES**

the specification of which

\_\_\_\_\_ is attached hereto, or

  X   was filed on January 15, 2004 as U.S. Patent Application Serial No. 10/760,661.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claim(s), as amended by any amendment referred to above.

I acknowledge the duty to disclose information that is material to patentability as defined in 37 C.F.R. § 1.56, including, for continuation-in-part applications, material information that became available between the filing date of the prior application and the national or PCT international filing date of the continuation-in-part application.

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or (f) or 365(b) of any foreign application(s) for patent, inventor's or plant breeder's rights certificate(s), or 365(a) of any PCT international application that designated at least one country other than the United States of America, listed below, and also have identified below any foreign application for patent, inventor's or plant breeder's rights certificate(s), or any PCT international application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application Number(s)	Country	Foreign Filing Date (MM/DD/YYYY)	Priority Claimed?	Certified Copy Attached?

**DIRECT COMMUNICATIONS TO:**

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. § 1001, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Full name of sole or first joint inventor: MARK L. JONES

Inventor's signature: Mark L Jones Date: 7/20/04

Residence: 9880 S.W. Ventura Court, Portland, Oregon 97223

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Full name of second joint inventor: ANSGAR KOCH

Inventor's signature: \_\_\_\_\_ Date: \_\_\_\_\_

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Full name of third joint inventor: FUMITOSHI ITO

Inventor's signature: Fumitoshi Ito Date: June 28, 2004

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Full name of fourth joint inventor: JAMES J. SHEN

Inventor's signature: 

Date: 7/19/04

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Full name of sole or first joint inventor: MARK L. JONES

Inventor's signature: \_\_\_\_\_ Date: \_\_\_\_\_

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Inventor's signature: Ansgar Koch Date: 29<sup>th</sup> June, 2004

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Inventor's signature: \_\_\_\_\_ Date: \_\_\_\_\_

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**POWER OF ATTORNEY,  
STATEMENT UNDER 37 C.F.R. § 3.73(b), and  
ASSERTION OF RIGHT UNDER 37 C.F.R. § 3.71 AND MPEP § 106**

Applicants: Mark L. Jones, Ansgar Koch, Fumitoshi Ito and James J. Shen  
Serial No.: 10/760,661  
Filed: January 15, 2004  
Title: METHOD OF ANALYZING A WAFER FOR METAL IMPURITIES

Siltronic Corporation, a Delaware corporation, hereby appoints the practitioners at Kolisch Hartwell, P.C., USPTO Customer No. 23581, as its attorneys and agents to prosecute the above-identified patent application and to transact all business in the United States Patent and Trademark Office connected therewith. These practitioners currently include:

J. Pierre Kolisch, Reg. No. 15,802; John M. McCormack, Reg. No. 26,948; Peter E. Heuser, Reg. No. 27,902; David P. Cooper, Reg. No. 33,372; Pierre C. Van Rysselberghe, Reg. No. 33,557; Walter W. Karnstein, Reg. No. 35,565; Charles H. DeVoe, Reg. No. 37,305; David S. D'Ascenzo, Reg. No. 39,952; Owen W. Dukelow, Reg. No. 41,002; James R. Abney, Reg. No. 42,253; Mark D. Alleman, Reg. No. 42,257; M. Matthews Hall, Reg. No. 43,653; Christopher S. Tuttle, Reg. No. 41,357; B. Anna McCoy, Reg. No. 46,077; Ellen M. Gonzales, Reg. No. 44,128; Jason C. Creasman, Reg. No. 51,587; Edward B. Anderson, Reg. No. 30,154; Peter D. Sabido, Reg. No. 50,353; John D. Russell, Reg. No. 47,048; Douglas W. McArthur, Reg. No. 50,795; Stanley M. Hollenberg, Reg. No. 47,658; and Anton E. Skaugset, Reg. No. 38,617.

Siltronic Corporation states, under 37 C.F.R. § 3.73(b), that it is the assignee of the entire right, title, and interest in the above-identified patent application by virtue of an assignment from each of the inventor(s) named in the above-identified patent application. Copies of assignments or other documents in the chain of title are attached.

Siltronic Corporation hereby asserts its right, under 37 C.F.R. § 3.71 and MPEP § 106, to conduct the prosecution of the above-identified patent application to the exclusion of the named inventor(s) and any previous assignee(s).

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The undersigned (whose title is supplied below) is authorized to act on behalf of the above-identified assignee.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements are made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. § 1001, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

**SILTRONIC CORPORATION**

Date: 15 July 2004

James G. Moreland  
Name: Dr. James A. Moreland

Title: V.P. Strategic Technology Development